

# WORKMAN | NYDEGGER

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PATENT

Docket No. 14993.31

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

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N

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OF COUNSEL

**REQUEST FOR A CERTIFICATE OF CORRECTION**  
**UNDER 37 C.F.R. § 1.323**

Attn: Certificate of Correction Branch  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. § 1.323 which implement 35 U.S.C. § 255, the Commissioner is respectfully requested to issue a Certificate of Correction to correct mistakes in the above-identified patent. The enclosed Patent

09/28/2005 EAYALEW1 00000054 6909305

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100.00 ₦P

\* Admitted only in California  
§ Admitted only in Virginia

Office Form 1050 reflects the desired corrections. Also enclosed please find a check in the amount of \$100.00 to cover the costs for issuance of this certificate.

The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 23-3178. Duplicate copies of this letter are enclosed.

Dated this 19<sup>th</sup> day of September, 2005.

Respectfully submitted,

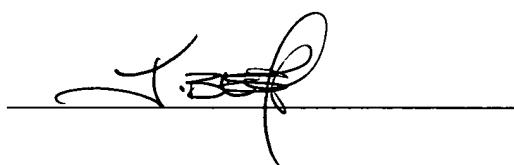


Adrian J. Lee  
Attorney for Patentee  
Registration No. 42,785  
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AJL:jrb  
Docket: 14993.31  
PR0000001148V001

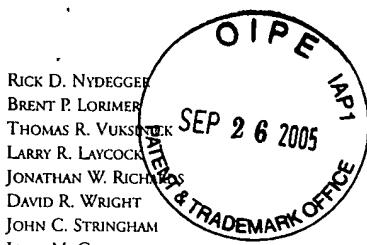
CERTIFICATE OF DEPOSIT UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 23 day of September, 2005.



Transmitted: REQUEST FOR A CERTIFICATE OF CORRECTION UNDER 37 C.F.R. § 1.323,  
PTO Form 1050 and Check No. 143991 for \$100.00

OCT 4 2005



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PATENT  
Docket No. 14993.31

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of



Li et al. )  
Serial No.: 10/673,840 ) Art Unit  
Filed: August 8, 2003 ) 2819  
For: DIGITALLY CONTROLLED IMPEDANCE )  
DRIVER MATCHING FOR WIDE VOLTAGE )  
SWINGS AT INPUT/OUTPUT NODE AND )  
HAVING PROGRAMMABLE STEP SIZE )  
Examiner: Vibol Tan )  
Patent No.: 6,909,305 B1 )  
Issue Date: June 21, 2005 )

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VERNON R. RICE §  
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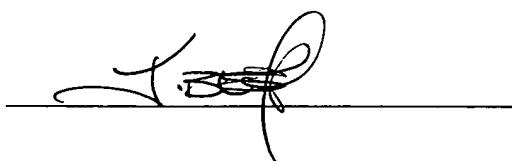


Adrian J. Lee  
Attorney for Patentee  
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AJL:jrb  
Docket: 14993.31  
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OCT 4 2005

# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO. : 6,909,305 B1

DATED : June 21, 2005

INVENTOR(S) : Li et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 2,

Line 8, change "assumes" to --assume--

Line 50, before "significant" insert --a--

Line 52, before "wider" insert --a--

Column 3,

Line 62, after "power-up phase" change "that" to --than--

Column 8,

Line 13, after "in such" insert --a--

Line 47, after "significant" change "that" to --than--

Column 9,

Line 26, change "flip-flop" to --flip-flops--

Line 63, change "way," to --ways,--

Column 11,

Line 14, change "process," to --processes,--

Column 12,

Line 36, after "input/output node," remove [width]

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PATENT NO. 6,909,305 B1

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